

PATENT

Attorney Docket No. 00-103

Express Mail Label No. EV 249895818 US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

RECEIVED

In re Application of:
Margaret S. Fyfield

) Group Art Unit: 2812

MAR 28 2003

Serial No. 09/731,596

) Confirmation No.: 9586

OFFICE OF PETITIONS

Filed: December 6, 2000

) Examiner:

For: PROBING FIXTURE AND METHOD FOR
SEMICONDUCTOR WAFERResponse to Notice to File Corrected Application PapersHonorable Commissioner of Patents and Trademarks
Washington, D.C. 20231

Sir:

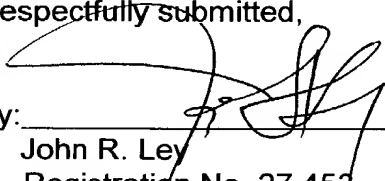
In response to the Notice to File Corrected Application Papers dated December 6, 2003, submitted herewith are formal drawings; and Part 2 of the Notice. Please note that in addition to formalizing the drawings, we have replaced the photograph of the prior art with a line drawing, see Fig. 1. The new line drawing uses the same reference numbers as in the photograph and refers to the same parts of the device.

Respectfully submitted,

Date:

3/25/03

By:


 John R. Ley
 Registration No. 27,453
 ATTORNEY FOR APPLICANT

JOHN R. LEY, LLC
 5299 DTC Blvd., Suite 610
 Greenwood Village, Colorado 80111-3321
 Telephone: (303) 740-9000
 Facsimile: (303) 740-9042

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CERTIFICATE OF MAILING BY EXPRESS MAIL

OFFICE OF PETITIONS

Honorable Commissioner of Patents and Trademarks
Washington, D.C. 20231

Sir:

The undersigned hereby certifies that the attached Petition to Revive Unintentionally Abandoned Application under 37 CFR 1.137(b); Notice of Abandonment under 37 CFR 1.53(f) or (g); Response to Notice to File Corrected Application Papers; Notice to File Corrected Application Papers; Four (4) Sheets of Drawings Certificate of Mailing by Express Mail; and Return Card, relating to the above application, were deposited as "Express Mail," Mailing Label No. EV249895818US, with the United States Postal Service, addressed to The Commissioner of Patents and Trademarks, Washington, D.C. 20231, on this 25th day of March, 2003.

3/25/03
Date

3/25/03
Date

Matthew M. Porter
Mailer

John R. Ley
Registration No. 27,453
ATTORNEY FOR APPLICANT

JOHN R. LEY, LLC
5299 DTC Boulevard, Suite 610
Greenwood Village, Colorado 80111-3327
Telephone: (303) 740-9000
Facsimile: (303) 740-9042



UNITED STATES PATENT AND TRADEMARK OFFICE

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WASHINGTON, D.C. 20231
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APPLICATION NUMBER	FILING/RECEIPT DATE	FIRST NAMED APPLICANT	ATTORNEY DOCKET NUMBER
09/731,596	12/06/2000	Margaret S. Fyfield	00-103

CONFIRMATION NO. 9586

FORMALITIES LETTER



OC000000005788516

Ralph R. Veseli, Esq.
Intellectual Property Law Department
LSI Logic Corporation, M/S D-106
1551 McCarthy Boulevard
Milpitas, CA 95035



Date Mailed: 02/22/2001

NOTICE TO FILE CORRECTED APPLICATION PAPERS

Filing Date Granted

This application has been accorded an Application Number and Filing Date. The application, however, is informal since it does not comply with the regulations for the reason(s) indicated below. Applicant is given TWO MONTHS from the date of this Notice within which to correct the informalities indicated below. Extensions of time may be obtained by filing a petition accompanied by the extension fee under the provisions of 37 CFR 1.136(a)

The required item(s) identified below must be timely submitted to avoid abandonment:

- Substitute drawings in compliance with 37 CFR 1.84 because:
 - drawing sheets do not have the appropriate margin(s) (see 37 CFR 1.84(g)). Each sheet must include a top margin of at least 2.5 cm. (1 inch), a left side margin of at least 2.5 cm. (1 inch), a right side margin of at least 1.5 cm. (5/8 inch), and a bottom margin of at least 1.0 cm. (3/8 inch);

*A copy of this notice **MUST** be returned with the reply.*

Customer Service Center
Initial Patent Examination Division (703) 308-1202

PART 2 - COPY TO BE RETURNED WITH RESPONSE

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MAR 28 2003

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